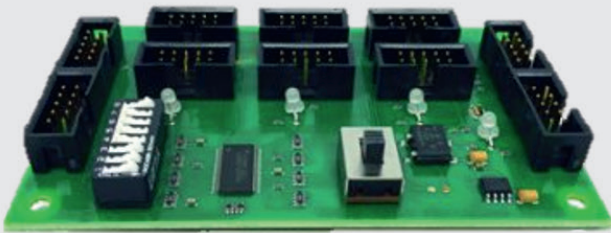


JT 2154 ScanBridge

An experiment board based on National Semiconductor's STA111 ScanBridge device



- ✓ **Three secondary-TAPs with LED activity indicators**
- ✓ **Standard JTAG 10-way IDC connector**
- ✓ **Features industry standard STA111 chip**
- ✓ **64 address options via on board DIP switch**

The JT 2154 is an experiment board based on National Semiconductor's STA111 ScanBridge device, aimed at users looking to utilise addressable TAP (Test Access Port) bridges/multiplexors within their designs to set-up 'system level' JTAG access.

The unit can also be used in semi-permanent installations to expand the TAP count on JTAG Technologies' controllers in order to address highly segmented designs.

Built-in support for ScanBridge-type devices within *ProVision* allows easy set-up of TAP assignments and device addressing.

Global Representation

Want to know more about our solutions and products?
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