The JTAGLive Studio tool-suite is a powerful yet low-cost collection of JTAG/boundary-scan programs devised to simplify hardware validation, board rework and repair and also enable small-scale PCB production test and device programming.

Interactive Testing and Validation
JTAGLive Studio is supplied complete with a state-of-the-art USB controller interface and a quick-start automatic scan chain detection system to get you up and running fast. Use the Buzz and BuzzPlus modules to then easily check individual connections on your boards interactively and Clip to add sequences of test patterns (vectors) to help debug logic clusters. Interactive use of optional low-cost CoreCommander modules enables hardware debugging of connected peripherals using your CPU’s debug mode – all without writing any code.

Automated Testing and Programming
Automatic boundary-scan pin interconnections testing is made possible through the AutoBuzz system that can learn a connectivity map from either a known good board or a CAD derived netlist. Extend your test coverage with logic cluster tests prepared using Script – a Python-powered boundary-scan test environment. Reusable Script modules can be shared between Studio license holders via the jtaglive.com forum. Add system-speed, kernel-centric tests for CPU-based or FPGA-based designs with CoreCommander options that can control CPU cores and/or link to FPGA gate array fabric. In-system programming of flash memories and serial PROMs is also supported through Script modules, while CPLD and FPGA programming is achieved using the included SVF, JAM and STAPL player programs.

Application Sequencing
Combine your interconnection test, your cluster tests, optional CoreCommander tests and your in-system programming actions for Flash and Programmable Logic (FPGA, CPLD’s) in a single Script application. By harnessing the features of the open-source Python environment you can create a customised test and programming sequence for your board and in the right format for your production environment.
JTAG Live Studio

JTAG Live Buzz
JTAG Live™ Buzz provides an easy solution for debugging boards built with BGA’s and too crowded for traditional probing with scopes or logic analysers - what’s more it’s totally free. Buzz is ideal for electronics engineers and technicians to use in checking printed circuit boards for basic continuity and correct operation. Buzz simply uses the built-in pin access provided in boundary-scan (IEEE Std 1149.1) compliant devices to perform pin to pin continuity tests [Buzz-outs] and can also sample pin activity on input pins.

JTAG Live BuzzPlus
While Buzz allows the user to sample activity on boundary-scan pins and test continuity of nets across a circuit board, BuzzPlus extends its capability with a unique ‘seek and discover’ mode that effectively learns the network of nodes for a specified net connection.

JTAG Live AutoBuzz
AutoBuzz is a totally unique tool that effectively learns a connectivity map of all boundary-scan parts within a design from only the BSDL models of those parts. By expanding on the seek and discover mode of BuzzPlus, AutoBuzz automatically gathers the circuit data of a known good board and then performs a full connectivity compare against the faulty circuit.

JTAG Live Clip
Clip is the vector-based upgrade for creating and saving board-level tests. It features unlimited pattern depth and bit width. Clip’s logic analyzer-like waveform display shows you what’s happening with the signals on your board. Use Clip’s “Compare On” and “Set Breakpoint” options for complete execution control.

JTAG Live Script
Script uses the open-source Python™ language to provide a powerful command and control structure to manipulate and sense cluster I/Os. With Script, you’ll use the Python code to verify operation of non-boundary-scan clusters. By means of high-level routines that can be embedded in a Python program, Script drives and senses values on pins or groups of pins. The Python editor facilitates preparation of the sequences to perform tests and collect results. Creating test modules in Script promotes device orientated testing and hence re-use of test code. Using Python open source means that thousands of auxiliary routines can be obtained from the established user community.

JAM/STAPL/SVF Players
The JAM/STAPL/SVF Players are used for programming FPGA’s using the JAM, STAPL or SVF files generated by FPGA development tools. The players can be called from Script to perform the programming actions.

JTAG Live CoreCommander
CoreCommander routines take control of key processor core (e.g. ARM, PPC, X-scale, Cortex etc.) functions using the built-in emulation/debug functions found in many of today’s microprocessor cores. They have been developed to speed-up board testing and debug by enabling kernel-centric testing. CoreCommander offers two modes of operation: Interactive - offering direct control of the core or; Python embedded - where controls can be scripted into a complete program.

JTAG Live Controller
The JTAG Live Controller is USB connected and powered and features a single test access port in JTAG Technologies standard pin-out. It offers a maximum programmable TCK speed of 6 MHz and also features programmable output voltages and input thresholds.

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