

JT 3705/USB - 'EXPLORER' 2 TAP CONTROLLER

Portable JTAG/boundary-scan controller for test and programming PCBAs



JT 3705/USB

- Two independent TAPs (Test Access Ports) for flexible board interfacing
- Fully Compatible with IEEE 1149.1, IEEE 1149.6, IEEE 1149.4 and IEEE 1532 standards
- Fully programmable voltage TAP (1.5V to 3.6V)
- Up to 6MHz programmable TCK frequency
- USB Powered (no external PSU needed)
- For boundary-scan /JTAG testing PLD & FPGA downloads and small-scale flash programming
- JTAG ProVision™ compatible
- Stylish contemporary design

Introduction

To address the steady growth of JTAG/boundary-scan (IEEE Std 1149.1) devices in electronic designs. JTAG Technologies has developed the only truly low-cost, fully functional boundary-scan controller featuring two test access ports (TAPs). The market for boundary-scan tools has evolved greatly throughout the 21st century and JTAG/boundary-scan logic is now fitted as standard to all CPLD's and most 'processors.

Multiple TAPs

The new JT 3705/USB is USB powered, making it easily portable. Its two TAPs can be fully synchronised for test purposes and it is compatible with all JTAG Technologies' developer tools such as JTAG ProVision and JTAG Live. Within ProVision multiple JT 3705s can be 'linked' to provide even higher TAP counts. The JT 3705/USB can be used equally for structural board testing or in-system device programming (ISP) of CPLDs flash memories etc..

Programmable levels

Programmable output levels per TAP (1.5V to 3.6V) make the JT3705/USB compatible with all current and future logic families. Test clock frequency may also be programmed from 1KHz to a brisk 6MHz allowing test applications to be executed in a 'snap'.

Software options

With a JT 3705/USB and a basic ProVision Platform set-up SMEs can equip themselves with an affordable yet potent structural test system. Alternatively team the JT 3705/USB with Python-based JTAGLive Script or Studio for a powerful JTAG functional test system.

This product represents a significant breakthrough and puts boundary-scan test within reach of many more would-be users.

Ordering information

JT 3705/USB - Explorer - fast two TAP boundary-scan controller/ programmer.

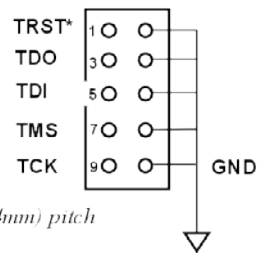
JT 3705/USB - 'EXPLORER' 2 TAP CONTROLLER

Portable JTAG/boundary-scan controller for test and programming PCBAs

The image shows a logic analyzer window displaying JTAG signals (TCK, TMS, TDI, TDO, TRST*) and a Verilog code editor for the JT3705 device. The code defines the device's internal structure, including address and data buses, and implements the JTAG protocol logic.

TAP connections

Test Access Port - TAP pin-out



IDC format connector 0.1" (2.54mm) pitch

Region or Country	Telephone	E-mail
• North America	Toll free - 877 FOR JTAG Western US - 949 454 9040	info@jtag.com
• Europe and Rest of World	+31 (0)40 295 0870	info@jtag.nl
• Finland	+358 (0)9 4730 2670	finland@jtag.com
• Germany	+49 (0)971 6991064	germany@jtag.com
• Sweden	+46 (0)8 754 6200	sweden@jtag.com
• United Kingdom & Ireland	+44 (0)1234 831212	sales@jtag.co.uk
• China, Malaysia, Singapore, Thailand, Taiwan	+86 (021) 5831 1577	info@jtag.com.cn

2102-E-2018-1000

© 2018 The JTAG Technologies logo and other trademarks designed with the symbol "®" are trademarks of JTAG Technologies registered in Europe and/or other countries. JTAG Technologies reserves the right to change design and specifications without notice.

